

Product Specification

NHD-C12864DA-FSRGB-FBW-3V3

COG (Chip-On-Glass) Liquid Crystal Display Module

NHD-	Newhaven Display
C12864-	128 x 64 Pixels
DA-	Model
F-	Transflective
SRGB-	Side LED Backlights (Red-Green-Blue)
F-	FSTN (+)
B-	6:00 Optimal View
W-	Wide Temperature
3V3-	3.0V LCD, 3.0V Backlight

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Additional Resources

- **Support Forum:** <https://support.newhavendisplay.com/hc/en-us/community/topics>
- **GitHub:** <https://github.com/newhavendisplay>
- **Example Code:** <https://support.newhavendisplay.com/hc/en-us/categories/4409527834135-Example-Code/>
- **Knowledge Center:** https://www.newhavendisplay.com/knowledge_center.html
- **Quality Center:** https://www.newhavendisplay.com/quality_center.html
- **Precautions for using LCDs/LCMs:** <https://www.newhavendisplay.com/specs/precautions.pdf>
- **Warranty / Terms & Conditions:** <https://www.newhavendisplay.com/terms.html>



Document Revision History

Revision	Date	Description	Changed By
-	04/02/2025	Initial Release	KL

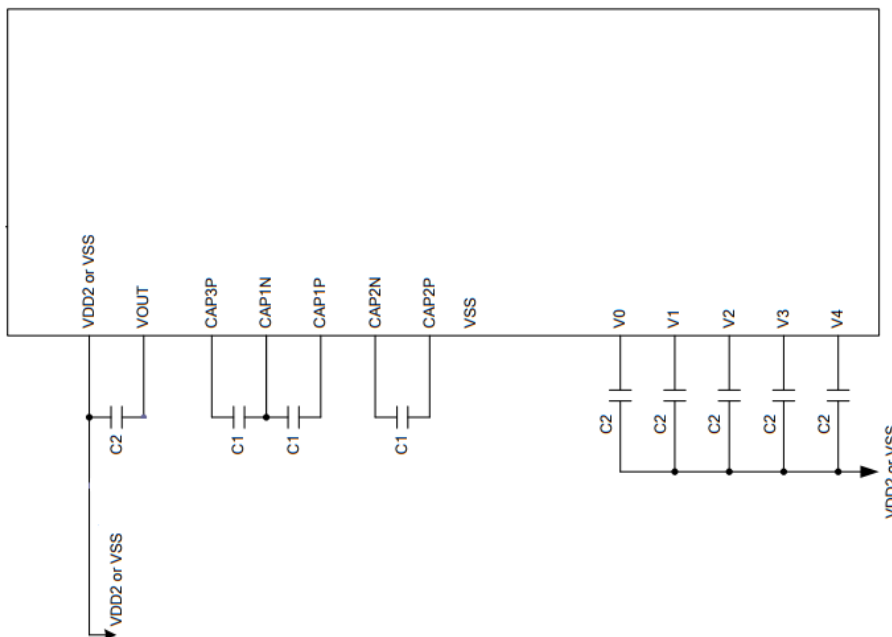
Pin Description

Pin No.	Symbol	External Connection	Function Description
1	CS1B	MPU	Active LOW Chip select
2	/RES	MPU	Active LOW Reset signal
3	A0	MPU	Register select signal. A0=1: Data; A0=0: Command
4	R/W /WR	MPU	6800 Mode: Read/Write select signal. R/W=1: Read R/W=0: Write 8080 Mode: Active LOW Write signal
5	E /RD	MPU	6800 Mode: Active High Enable signal 8080 Mode: Active LOW Read signal
6-13	D0-D7	MPU	Parallel interface: 8-bit directional data bus Serial interface: DB0-DB5 = No Connect D6=Serial clock (SCL) D7= Serial Data (SI)
14	VDD	Power Supply	Supply voltage for LCD and logic (+3.0V)
15	VSS	Power Supply	Ground
16	VOOUT	Power Supply	Connect 1 μ F – 2.2 μ F Capacitor to VDD or VSS
17	CAP3+	Power Supply	Connect 1 μ F – 2.2 μ F Capacitor to CAP1- (pin 18)
18	CAP1-	Power Supply	Connect 1 μ F – 2.2 μ F Capacitor to CAP1+ (pin 19) and CAP3+ (pin 17)
19	CAP1+	Power Supply	Connect 1 μ F – 2.2 μ F Capacitor to CAP1- (pin 18)
20	CAP2+	Power Supply	Connect 1 μ F – 2.2 μ F Capacitor to CAP2- (pin 21)
21	CAP2-	Power Supply	Connect 1 μ F – 2.2 μ F Capacitor to CAP2+ (pin 20)
22-26	V4-V0	Power Supply	0.1 μ F – 1 μ F Capacitor to VDD or VSS
27	C86	MPU	MPU Interface select signal. C86=1: 6800; C86=0: 8080
28	P/S	MPU	Interface Mode select signal. P/S=1: Parallel; P/S=0: Serial
10	K-RED	Power Supply	Cathode Red (Ground)
11	K-GREEN	Power Supply	Cathode Green (Ground)
12	K-BLUE	Power Supply	Cathode Blue (Ground)
13	LED-A	Power Supply	Common Anode for LEDs (60mA)

Recommended LCD connector: 1.27mm pitch thru-hole connection on PCB.

Recommended Breakout Board: NHD-PCB40

Wiring Diagram



Electrical Characteristics

Item	Symbol	Condition	Min.	Typ.	Max.	Unit
Operating Temperature Range	T _{OP}	Absolute Max	-20	-	+70	°C
Storage Temperature Range	T _{ST}	Absolute Max	-30	-	+80	°C
Supply Voltage	V _{DD}	-	2.7	3.0	3.3	V
Supply Current	I _{DD}	V _{DD} = 3.0V T _{OP} = 25°C	-	0.5	1.5	mA
Supply for LCD (contrast)	V _{LCD}		8.6	8.8	9.0	V
"H" Level input	V _{IH}	-	0.8 * V _{DD}	-	V _{DD}	V
"L" Level input	V _{IL}	-	V _{SS}	-	0.2 * V _{DD}	V
"H" Level output	V _{OH}	-	0.8 * V _{DD}	-	V _{DD}	V
"L" Level output	V _{OL}	-	V _{SS}	-	0.2 * V _{DD}	V
Backlight Supply Voltage – RED	V _R	I _R = 60mA	2.6	3.2	3.8	V
Backlight Supply Current – RED	I _R	-	-	60	-	mA
Backlight Supply Voltage – GREEN	V _G	I _G = 60mA	2.6	3.2	3.8	V
Backlight Supply Current – GREEN	I _G	-	-	60	-	mA
Backlight Supply Voltage – BLUE	V _B	I _B = 60mA	2.6	3.2	3.8	V
Backlight Supply Current – BLUE	I _B	-	-	60	-	mA

Optical Characteristics

Item		Symbol	Condition	Min.	Typ.	Max.	Unit
Optimal Viewing Angles	Top	φY+	CR ≥ 2	-	40	-	°
	Bottom	φY-		-	30	-	°
	Left	θX-		-	30	-	°
	Right	θX+		-	30	-	°
Contrast Ratio		CR	-	2	4.1	-	-
Response Time	Rise	T _R	-	-	155	250	ms
	Fall	T _F		-	185	300	ms

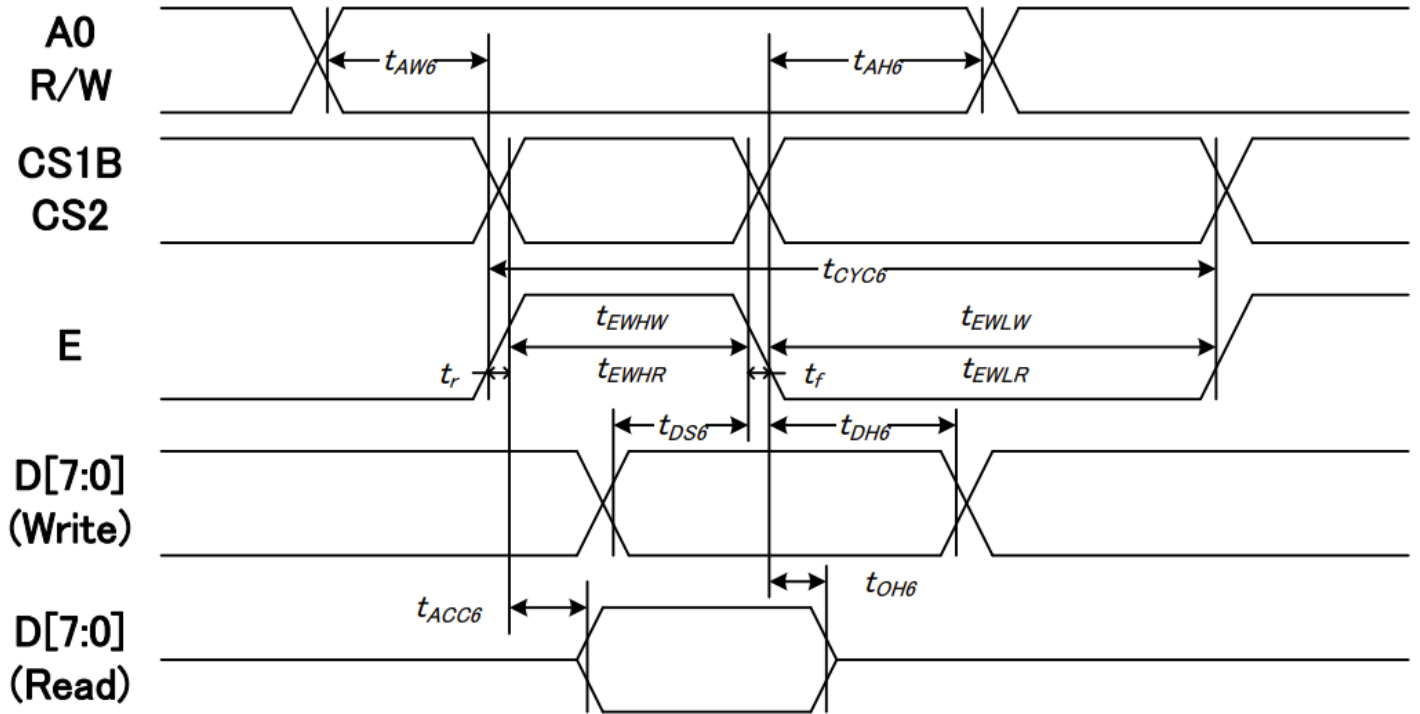
Controller Information

Built-in ST7565R Controller: <https://support.newhavendisplay.com/hc/en-us/articles/4414899357591-ST7565R>



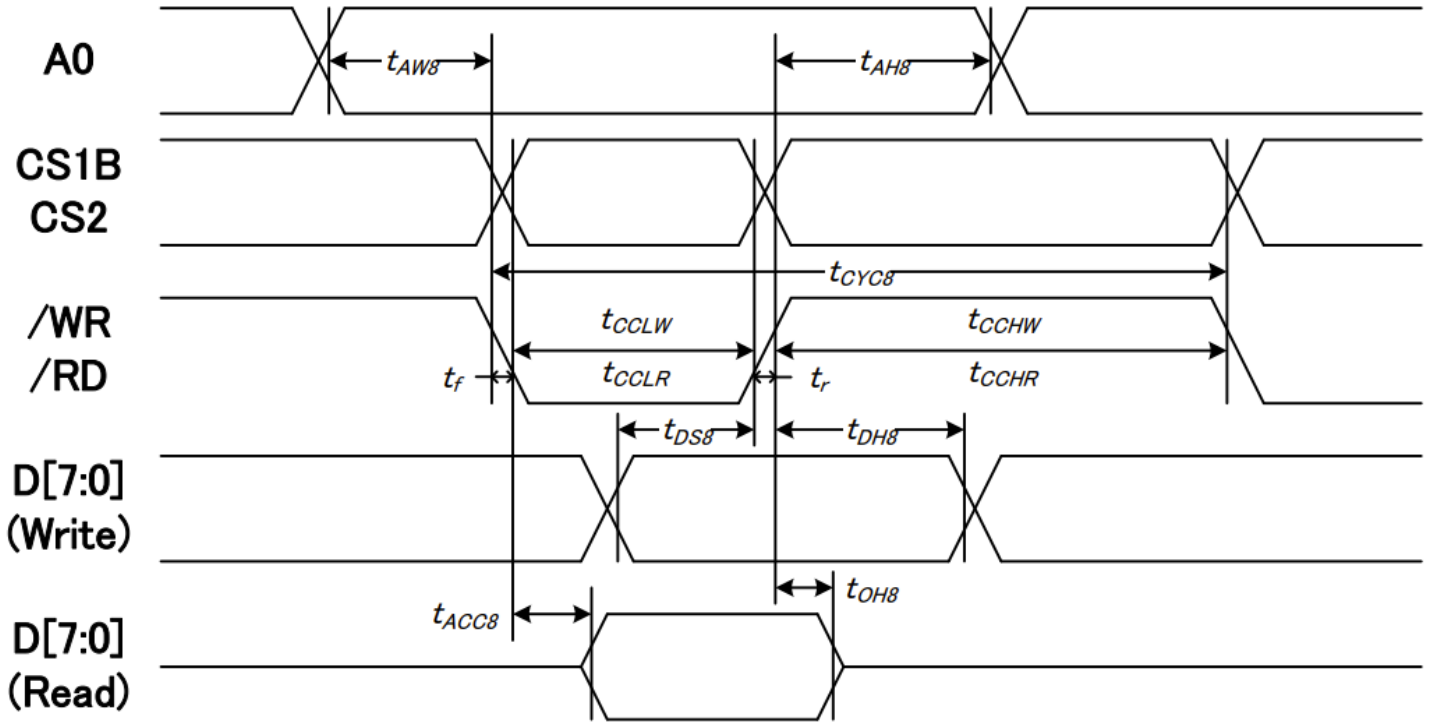
Timing Characteristics

System Bus Timing for 6800 Series MPU



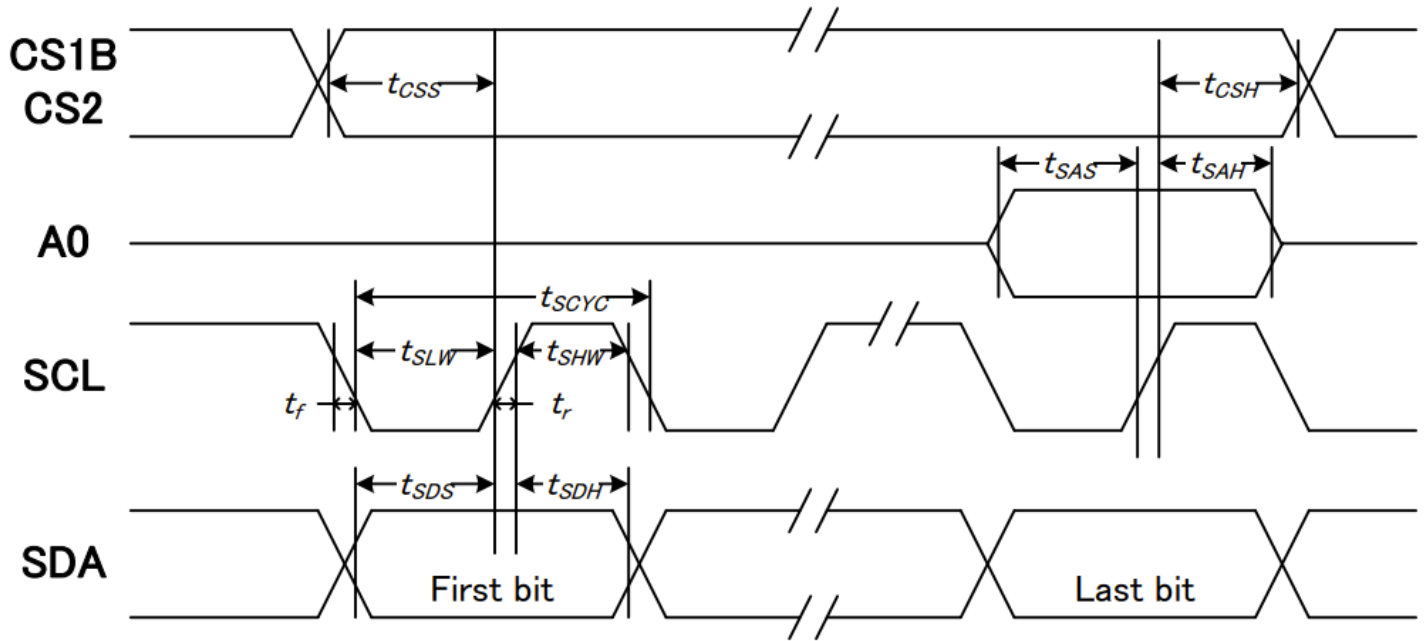
Item	Signal	Symbol	Condition	Min.	Max.	Unit
Address setup time	A0	t_{AW6}		0	—	ns
Address hold time		t_{AH6}		0	—	
System cycle time	E	t_{CYC6}		240	—	
Enable L pulse width (WRITE)		t_{EHLW}		80	—	
Enable H pulse width (WRITE)		t_{EHLH}		80	—	
Enable L pulse width (READ)		t_{EHLR}		80	—	
Enable H pulse width (READ)	D[7:0]	t_{EHLH}		140	—	
Write data setup time		t_{DS6}		40	—	
Write data hold time		t_{DH6}		10	—	
Read data access time		t_{ACC6}	CL = 100 pF	—	70	
Read data output disable time		t_{OH6}	CL = 100 pF	5	50	

System Bus Timing for 8080 Series MPU



Item	Signal	Symbol	Condition	Min.	Max.	Unit
Address setup time	A0	tAW8		0	—	ns
Address hold time		tAH8		0	—	
System cycle time	/WR	tCYC8		240	—	
/WR L pulse width (WRITE)		tCCLW		80	—	
/WR H pulse width (WRITE)		tCCHW		80	—	
/RD L pulse width (READ)	RD	tCCLR		140	—	
/RD H pulse width (READ)		tCCHR		80	—	
WRITE Data setup time	D[7:0]	tDS8		40	—	
WRITE Data hold time		tDH8		20	—	
READ access time		tACC8	CL = 100 pF	—	70	
READ Output disable time		tOH8	CL = 100 pF	5	50	

System Bus Timing for 4-Wire SPI

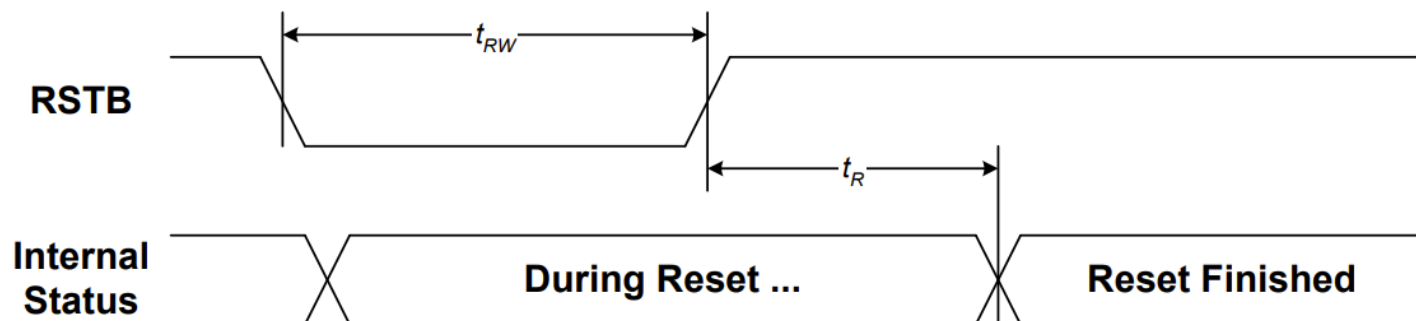


Item	Signal	Symbol	Condition	Min.	Max.	Unit
Serial clock period	SCLK	tSCYC		50	—	ns
SCLK "H" pulse width		tSHW		25	—	
SCLK "L" pulse width		tSLW		25	—	
Address setup time	A0	tSAS		20	—	
Address hold time		tSAH		10	—	
Data setup time	SDA	tSDS		20	—	
Data hold time		tSDH		10	—	
CS-SCLK time	CS1B	tCSS		20	—	
CS-SCLK time	CS2	tCSH		40	—	

*1 The input signal rise and fall time (t_r , t_f) are specified at 15 ns or less.

*2 All timing is specified using 20% and 80% of V_{DD} as the standard.

Reset Timing



Item	Symbol	Condition	Min.	Max.	Unit
Reset time	tR		—	1.0	us
Reset “L” pulse width	tRW		1.0	—	

Example Initialization Program

```
void data_out(unsigned char i) //Data Output Serial Interface
{
    unsigned int n;
    CS = 0;
    A0 = 1;
    for(n=0; n<8; n++){
        i <<=1;
        SCL = 0;
        P1 = i;
        delay(2);
        SCL = 1;
    }
    CS = 1;
}

void comm_out(unsigned char j) //Command Output Serial Interface
{
    unsigned int n;
    CS = 0;
    A0 = 0;
    for(n=0; n<8; n++){
        j <<=1;
        SCL = 0;
        P1 = j;
        delay(2);
        SCL = 1;
    }
    CS = 1;
}

/*****
*      Initialization For controller      *
*****/
void init_LCD()
{
    comm_out(0xA0);
    comm_out(0xAE);
    comm_out(0xC0);
    comm_out(0xA2);
    comm_out(0x2F);
    comm_out(0x21);
    comm_out(0x81);
    comm_out(0x3F);
}
/*****/
```

Quality Information

Test Item	Content of Test	Test Condition	Note
High Temperature storage	Endurance test applying the high storage temperature for a long time.	+80°C, 200hrs	2
Low Temperature storage	Endurance test applying the low storage temperature for a long time.	-30°C, 200hrs	1,2
High Temperature Operation	Endurance test applying the electric stress (voltage & current) and the high thermal stress for a long time.	+70°C, 200hrs	2
Low Temperature Operation	Endurance test applying the electric stress (voltage & current) and the low thermal stress for a long time.	-20°C, 200hrs	1,2
High Temperature / Humidity Operation	Endurance test applying the electric stress (voltage & current) and the high thermal with high humidity stress for a long time.	+60°C, 90% RH, 96hrs	1,2
Thermal Shock resistance	Endurance test applying the electric stress (voltage & current) during a cycle of low and high thermal stress.	-20°C, 30min -> 25°C, 5 min-> 70°C, 30min = 1 cycle 10 cycles	
Vibration test	Endurance test applying vibration to simulate transportation and use.	10-50Hz, 1.5mm amplitude. 2g Acceleration. 60 sec in each of 3 directions X, Y, Z for 30 minutes	3
Static electricity test	Endurance test applying electric static discharge.	Air: ±8kV	
		Contact: ±4kV	

Note 1: No condensation to be observed.

Note 2: Conducted after 4 hours of storage at 25°C, 0%RH.

Note 3: Test performed on product itself, not inside a container.